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			Applicant(s): VANDENHOECK et al.			
			Filing Date: Herewith		Group:	
U.S. PATENTS						
Initials	Patent Number	Issue Date	Name	Class	Sub-class	Filing date
U.S. PATENT PUBLICATIONS						
Initials	Publication No.	Pub. Date	Name	Class	Sub-class	Filing Date
/AE/	2001/0004761 A1	6/21/01	Zehavi	714	784	2/14/01
FOREIGN PATENT DOCUMENTS						
Initials	Document Number	Date	Country	Name	Translation? Yes/No/n/a	
/AE/	WO 01/93431 A1	12/6/01	PCT	Qualcomm Incorporated	n/a	
OTHER DOCUMENTS (Title, Author, Date, Pages, Etc., if known)						
/AE/	"A 16-Bit Digital Signal Processor With Specially Arranged Multiply-Accumulator For Low Power Consumption", Katsuhiko et al., IEICE Transactions on Electronics, Institute Of Electronics Information And Comm. Eng., Vol. E78-C, No. 12, 12/1/95, pp. 1709-1716.					
Examiner's Signature: /Aslan Ettehadieh/				Date Considered: 03/02/2007		
Initial if reference was considered, whether or not citation is in conformance with MPEP. Mark through citation if not considered. Include a copy of this citation form with your next correspondence to the Applicant(s).						

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